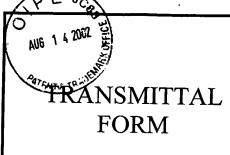
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Application Serial Number-	09/932,754		
Filing Date	August 17, 2001		
First Named Inventor	Kamieniecki		
Group Art Unit	2829		
Examiner Name	Hollington		
Attorney Docket No.	QCS-001DV3		
Patent No.	Not applicable		
Issue Date	Not applicable		
Attorney Docket No. Patent No.	Hollington  QCS-001DV3  Not applicable		

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		Exami	ner Name	$\neg \uparrow$	Hollington		
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		Patent	No.		Not applicable		
		Issue I	Date	$\neg$	Not applicable		
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Atty. Docket No. OCS-001DV3 (6695

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS:

Kamieniecki et al.

SERIAL NUMBER:

09/932,754

ART UNIT: 2829

FILING DATE:

August 17, 2001

EXAMINER: J. Hollington

TITLE:

Real-Time In-Line Testing Of Semiconductor Wafers

**Assistant Commissioner for Patents** Washington, D.C. 20231

## AMENDMENT and RESPONSE

Sir:

In response to the Office Action mailed on May 9, 2002, in connection with the patent application identified above, for which the period of response ends on August 9, 2002, Applicants respectfully request entry of this amendment and response, reconsideration, and passage of this application to allowance. In the event that any additional fees are due, the Commissioner is hereby authorized to charge any such fees to Attorney's Deposit Account No. 20-0531.

Please amend the application, without prejudice, as follows:

## In the Claims:

Please amend the following claim:

(Twice Amended) A semiconductor wafer fabrication system comprising: 53.

a sealed chamber for processing said semiconductor wafer having a first surface and a second surface; and a head assembly comprising:

a modulated light source exposing at least a portion of said semiconductor wafer to light having a wavelength and modulated at a frequency; and